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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/646,981	08/22/2003	Andrew Harker	30020778US02	3035	
7590 11/16/2004			EXAMINER		
Paul D. Greeley, Esq.			DOAN, JENNIFER		
Ohlandt, Greeley, Ruggiero & Perle, L.L.P. 10th Floor			ART UNIT	PAPER NUMBER	
One Landmark Square Stamford, CT 06901-2682			2874	2874	
			DATE MAILED: 11/16/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/646,981	HARKER, ANDREW				
Office Action Summary	Examiner	Art Unit				
,		2874				
The MAILING DATE of this communication app	Jennifer Doan ears on the cover sheet with the					
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 22 Au	ıgust 2003.					
	· · · · · · · · · · · · · · · · · · ·					
3) Since this application is in condition for allowar	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims	•					
4)⊠ Claim(s) <u>1-10</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-6 and 8-10</u> is/are rejected.	· _ · · · · · · · · · · · · · · · · · ·					
7)⊠ Claim(s) <u>7</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or	8) Claim(s) are subject to restriction and/or election requirement.					
Application Papers						
9)⊠ The specification is objected to by the Examiner.						
	10)⊠ The drawing(s) filed on <u>22 August 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary Paper No(s)/Mail D					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)		Patent Application (PTO-152)				
Paper No(s)/Mail Date <u>082203</u> . 6) Other:						

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#### **DETAILED ACTION**

## **Priority**

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### Information Disclosure Statement

2. The prior art documents submitted by applicant in the Information

Disclosure Statement filed on 08/22/2003, have all been considered and made of record (note the attached copy of form PTO-1449).

# **Drawings**

3. The drawings, filed on 08/22/2003, are accepted.

# Specification

4. Claims 1, 5, 9 and 10 are objected to because of the following informalities:

Claim 1, line 5, "the optical waveguide" should be changed to "the optical light guide" for clarifying the scope of the claim.

Claim 5, line 3, recites the limitation "the periphery of the substrate or the second substrate". There is insufficient antecedent basis for the limitation in the claim. It is suggested that the term "the periphery of the substrate or the second

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substrate" should be changed to "a periphery of a substrate or a second substrate".

Claim 9, line 2, "the optical waveguide" should be changed to "the optical light guide" for clarifying the scope of the claim.

Claim 10, lines 2-3, "the optical waveguide" should be changed to "the optical light guide" and "the waveguide" should be changed to "the light guide" for clarifying the scope of the claim.

Claim 10, line 3 recites the limitation "the substrate". There is insufficient antecedent basis for the limitation in the claim. It is suggested that the term "the substrate" should be changed to "a substrate".

Appropriate correction is required.

Applicant's cooperation is requested in correcting any other errors of which applicant may become aware in the specification.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35U.S.C. 102 that form the basis for the rejections under this section made in thisOffice action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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6. Claims 1-6 and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Naoki (JP 2001-326419) (translated by using Patent Abstracts of Japan website).

With respect to claim 1, Naoki (figures 2a and 2b) discloses an optoelectronic assembly comprising an optical emitter (21) for emitting light along a main path, at least one mouldable (see figure 2a), substantially rigid optical light guide (46) having a first end for receiving a small proportion of the light from the main optical path ([0027], lines 1-2) and a second end, and at least one photodetector (41) located adjacent the second end of the optical light guide (46) for receiving light there from ([0026], line 2).

With respect to claim 2, Naoki (figures 2a and 2b) discloses an optoelectronic assembly wherein the optical emitter (21), the at least one photodetector (41) and at least one optical light guide (46) are mounted on a substrate (24) and the at least one photodetector (41) is arranged at a periphery of the substrate (24).

With respect to claim 3, Naoki (figure 2b) discloses an optoelectronic assembly wherein the optical emitter (21) is mounted on a substrate (see figure 2b) and the photodetector (41) is mounted on a second substrate (see figure 2b).

With respect to claim 4, Naoki (figures 2a and 2b) discloses an optoelectronic assembly including a plurality of mouldable (see figure 2a),

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substantially rigid optical light guides (46, 47 and 48), and a plurality of photodetectors (41, 42 and 43), the plurality of optical light guides (46, 47 and 48) each having a second end located adjacent a respective one of the photodetectors (41, 42 and 43).

With respect to claim 5, Naoki (figure 2a) discloses an optoelectronic assembly wherein the plurality of photodetectors (41, 42 and 43) is mounted as an array (see figure 2a) adjacent a periphery of a substrate (24).

With respect to claim 6, Naoki (figure 2b) discloses an optoelectronic assembly wherein the plurality of optical light guides (46, 47 and 48) is manufactured as a single assembly for mounting to the substrate (24).

With respect to claim 10, Naoki (figures 2a and 2b) discloses an optoelectronic assembly wherein the optical waveguide (46) includes one or more fiducials to facilitate alignment of the waveguide (46) to the substrate (24).

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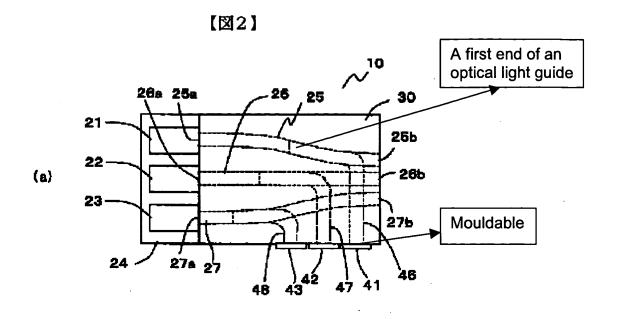


Figure 2a

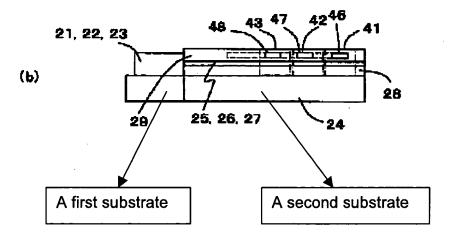


Figure 2b

### Claim Rejections - 35 USC § 103

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 8. Claims 8 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Naoki (as cited above) in view of Jiang et al. (U.S. Patent 5,774,486).

With respect to claim 8, Naoki (figures 2a and 2b) discloses an optoelectronic assembly, wherein the first end of the optical light guide (46) is positioned in the secondary light path.

Naoki does not specifically disclose means for splitting a small proportion of light from the main optical path into a secondary light path.

However, Jiang et al. (figure 4) disclose means for splitting a small proportion of light from the main optical path into a secondary light path (abstract, lines 2-4). Such an element would easily control and monitor the light emission. It would advantageously provide a power monitoring system having automatic control the emissions (column 1, lines 46-62). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to form means for splitting a small proportion of light from the main optical path

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into a secondary light path into Naoki's device (accordance with the teaching of Jiang et al.) for the purpose of easily controlling and monitoring the light emission and providing a power monitoring system having automatic control the emissions.

With respect to claim 9, Naoki substantially discloses an optoelectronic assembly of the claimed invention.

Naoki does not explicitly disclose the optical light guide is made from a stable, low absorption plastic material.

However, Jiang et al. disclose the optical light guide being made from plastic material (column 4, lines 19-20). The plastic material is inexpensive. It would advantageously provide an easy manufacture of power monitor system and automatic power control (column 1, lines 49-51). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to form the optical light guide made from a plastic material into Naoki's device (accordance with the teaching of Jiang et al.) for the purpose of facilitating the manufacture of the optical device with low cost and high efficiency.

# Allowable Subject Matter

9. Claim 7 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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The prior art of record fails to disclose or reasonably suggest the combination of the structure of the optoelectronic assembly comprising the optical light guide includes at least one structural feature to facilitate interception of the light from the main optical path as recited in claim 7.

#### · Conclusion

- 10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Buchter (U.S. Patent 6,536,957) discloses an integrated optical transceiver assembly for mating with optical fibers terminating in a connector.
- 11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer Doan whose telephone number is (571) 272-2346. The examiner can normally be reached on Monday to Thursday from 6:00 am to 3:30 pm, second Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rodney Bovernick can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-

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direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Jennifer Doan

Patent examiner

November 12, 2004